47.(New)   The memory of claim 46, wherein said accessing a first sector of said memory cells comprises programming data values into said memory cells of the first sector, wherein the data values are one of the first and second states

48.(New)   The memory of claim 46, wherein said accessing a first sector of said memory cells comprises reading the data values stored in said memory cells of the first sector, wherein the data values are one of the first and second states

49.(New)   The memory of claim 46, wherein said second sector is chosen at random prior to said identifying.

50.(New)   The memory of claim 46, wherein a memory cells of said first sector shares a common bit line with a memory cell of said second sector.--

866521 v1 / PF-OA [Rev. 000913]   Serial No. 09/437,078